

Form 1449*	Docket Number: G&C 31006.2-US-U1	Application Number: 09/938,978
INFORMATION DISCLOSURE STATEMENT ON AN APPLICATION	Applicant: Alan N. Willson, Jr. et al.	
	Filing Date: August 24, 2001	Group Art Unit: 2121

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILED DATE IF APPROPRIATE
W	4,746,899	05/24/88	Swanson et al.			
W	4,751,666	06/14/88	Gillingham			
W	5,953,241	09/14/99	Hansen et al.			
W	6,308,190 B1	10/23/01	Willson Jr. et al.			
FOREIGN PATENTS						
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)						
W		Chevillat, Pierre R. et al., "Optimum FIR Transmitter and Receiver Filters for Data Transmission over Band-Limited Channels," IEEE Transactions on Communications, Vol. Com-30, No. 8, August 1982, pp. 1909-1915.				
W		Kuo, Tzu-Chieh et al., "A Programmable Interpolation Filter for Digital Communications Applications," Proceedings of the 1998 IEEE International Symposium on Circuits and Systems, May 31-June 3, 1998, Monterey, CA, IEEE, 1998, Vol. II, pp. II-98 - II-100.				
W		Kuo, Tzu-Chieh, "A Programmable Interpolation Filter for Digital Communication Applications," Thesis, Master of Science in Electrical Engineering, University of California, Los Angeles, CA, 1996, pp. xi, 1-51.				
W		Moreau de Saint-Martin, Francois et al., "Design of Optimal Linear-Phase Transmitter and Receiver Filters for Digital Systems," IEEE, 1995, pp. 885-888.				
W		Proakis, John G. et al., "Digital Signal Processing: Principles, Algorithms, and Applications," Macmillian Publishing Company, New York, pp. 485-502, undated.				
W		Samueli, Henry, "On the Design of Optimal Equiripple FIR Digital Filters for Data Transmission Applications," IEEE Transactions on Circuits and Systems, Vol. 35, No. 12, December 1998, pp. 1542-1546.				
W		Samueli, Henry, "On the Design of FIR Digital Data Transmission Filters with Arbitrary Magnitude Specifications," IEEE Transactions on Circuits and Systems, Vol. 38, No. 12, December 1991, pp. 1563-1567.				
W		Thompson, Charles D. et al., "A Digitally-Corrected 20b Delta-Sigma Modulator," IEEE International Solid-State Circuits Conference, Session 11/Oversampled Data Conversion/Paper TP 11.5, IEEE 1994, pp. 194-195.				
W		Vaidyanathan, P.P., "Multirate Systems and Filter Banks," Prentice Hall, Englewood Cliffs, NJ 07631, pp. 118-134, undated.				
W		Kuskie C. et al. (1995) "A Decimation Filter Architecture for GHz Delta-Sigma Modulators," IEEE Int'l Symposium on Circuits and Systems, pp. 953-956.				
W		Nagari, A. et al., "A 2.7V 11.8 mW Baseband ADC with 72 dB Dynamic Range for GSM," 1999, IEEE, Custom Integrated Circuits Conference, pp. 133-136.				

EXAMINER: NGO	DATE CONSIDERED: 5/26/04
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.	

*Substitute Disclosure Statement Form (PTO-1449)

Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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